

ABSTRACT OF THE DISCLOSURE:

An address-counter control system includes a counter circuit, path
5 switches, and a control circuit. The counter circuit includes a first series
of address counters which corresponds to a non-contiguous region
portion and second and third series of address counters which
correspond to respective contiguous region portions and which are
located at two opposite ends of the first series of address counters. The
10 path switches are provided at connection paths between the second and
the third series of address counters. The path switches disconnect the
first series of address counters and directly connect the second and third
series of address counters or disconnect the direct connection between
the second and third series of address counters and connect the first
15 series of address counters to and between the second and the third
series of address counters. The control circuit control the path switches.